

GP 2811

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PTO/SB/21 (6-98)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM (Use for all correspondence after initial filing)	Application Number	09/652,550
	Filing Date	August 31, 2000
	First Named Inventor	Keiji Jono
	Group Art Unit	2811
	Examiner Name	Unknown
Total Number of Pages in This Submission	18	Attorney Docket Number KMI-001

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application TWO	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input checked="" type="checkbox"/> Power of Attorney, Revocation, Change of Correspondence Address KMT & MICRON	<input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	Postcard.
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Small Entity Statement	
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	No fees required.
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	The Commissioner is hereby authorized to charge any additional fees required under 37 CFR Sections 1.16 and 1.17 and credit any overpayments to: 23-0925.	

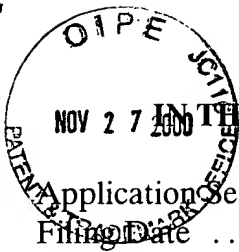
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Frederick M. Fliegel, Ph.D., Reg. No. 36,138 Wells, St. John, Roberts, Gregory & Matkin P.S. Customer No. 021567
Signature	
Date	Nov. 21, 2000

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 11/21/00	
Typed or printed name	Robin Saldivia
Signature	
Date	11/21/00

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#5/ Power
attorney
1/12/01
V/S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/652,550
Filing Date August 31, 2000
Inventor Keiji Jono et al.
Assignee Micron Technology, Inc. and KMT Semiconductor, Ltd.
Group Art Unit 2811
Examiner Unknown
Attorney's Docket No. KM1-001
Title: Methods of Forming an Isolation Trench in a Semiconductor, Methods of
Forming an Isolation Trench in a Surface of a Silicon Wafer, Methods of
Forming an Isolation Trench-Isolated Transistor, Trench-Isolated Transistor,
Trench Isolation Structures Formed in a Semiconductor, Memory Cells and
DRAMS

**POWER OF ATTORNEY BY ASSIGNEE AND
CERTIFICATE BY ASSIGNEE UNDER 37 CFR §3.73(b)**

RECEIVED
DEC 05 2000
TECHNOLOGY CENTER 2800

To: Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., a joint Assignee of part of the entire
right, title and interest in the above-identified patent application by assignment
attached hereto, hereby appoints the attorneys and agents of the firm of
WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S., listed as
follows:

David P. Roberts	Reg. No. 23,032
Randy A. Gregory	Reg. No. 30,386
Mark S. Matkin	Reg. No. 32,268
James L. Price	Reg. No. 27,376
Deepak Malhotra	Reg. No. 33,560
Mark W. Hendricksen	Reg. No. 32,356
David G. Latwesen	Reg. No. 38,533
George G. Grigel	Reg. No. 31,166
Keith D. Grzelak	Reg. No. 37,144



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Washington, D.C. 20231

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Keith D. Grzelak	Reg. No. 37,144

James D. Shaurette Reg. No. 39,833
Frederick M. Fliegel Reg. No. 36,138
Donald Brent Kenady Reg. No. 40,045
James E. Lake Reg. No. 44,854
Bernard Berman Reg. No. 37,279

and also attorneys Michael L. Lynch (Reg. No. 30,871) and Lia Pappas Dennison (Reg. No. 34,095) of Micron Technology, Inc., as its attorneys with full power of substitution to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

The Assignee certifies that the above-identified Assignment has been reviewed and to the best of Assignee's knowledge and belief, joint title is in the Assignee, and a copy of the Assignment is submitted herewith.

Please direct all correspondence regarding this application to:

Customer No. 021567
Wells, St. John, Roberts, Gregory & Matkin P.S.
Attn: Frederick M. Fliegel, Ph.D.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Telephone: (509) 624-4276
Facsimile: (509) 838-3424

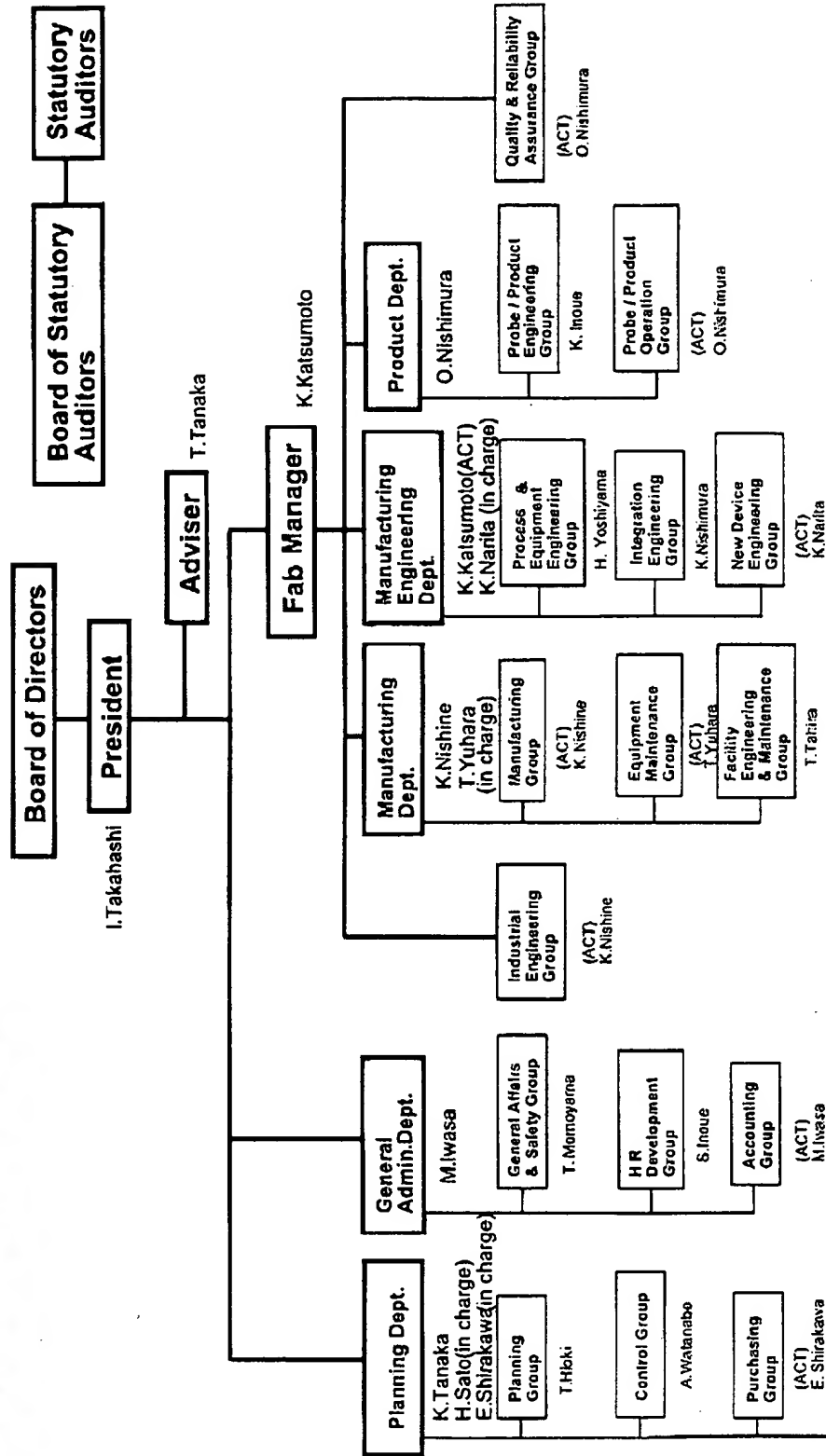
KMT SEMICONDUCTOR, LTD.

Dated: Nov 06, 2000 By: Ken-ichi Katsumoto
Name: Ken-ichi Katsumoto
Title: Board of Director

Attachments: Copy of Assignment and KMT Organization Chart

4

KMT Organization Chart



Jul 1st, 2000

KMT Strictly Private

ASSIGNMENT

PARTIES TO THE ASSIGNMENT:

INVENTORS:

KEIJI JONO

HIROKAZU UEDA

HIROYUKI WATANABE

JOINT ASSIGNEES:

Micron Technology, Inc.
Corporation of the State of Delaware
8000 South Federal Way
Boise, Idaho 83716

and

KMT Semiconductor, Ltd.
A Japanese Corporation
302-2, Hirano-cho, Nishiwaki-shi
HYOGO
677-0063 JAPAN

BACKGROUND OF THIS ASSIGNMENT:

INVENTORS have conceived certain new and useful inventions disclosed in a United States Patent Application Serial No. 09/652,550, filed August 31, 2000, titled Methods of Forming an Isolation Trench in a Semiconductor, Methods of Forming an Isolation Trench in a Surface of a Silicon Wafer, Methods of Forming an Isolation Trench-Isolated Transistor, Trench-Isolated Transistor, Trench Isolation Structures Formed in a Semiconductor, Memory Cells and DRAMS.

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DEC 05 2000
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1 MICRON TECHNOLOGY, INC. desires to acquire part of the entire
2 right, title and interest in said inventions and with respect to any Letters
3 Patent that may be granted with respect to the inventions in both the United
4 States and in all foreign countries.

5 KMT SEMICONDUCTOR, LTD. desires to acquire part of the entire
6 right, title and interest in said inventions and with respect to any Letters
7 Patent that may be granted with respect to the inventions in both the United
8 States and in all foreign countries.

9 **THE PARTIES AGREE AS FOLLOWS:**

10 In consideration of good and valuable consideration, the receipt
11 sufficiency and adequacy of which is hereby acknowledged, INVENTORS
12 hereby sell, assign and transfer to MICRON TECHNOLOGY, INC. and to
13 KMT SEMICONDUCTOR, LTD. the entire right, title and interest in the
14 above-identified application executed currently with this assignment and to any
15 reissues, renewals, divisions or continuations thereof, and hereby authorizes the
16 Commissioner of Patents and Trademarks to issue such Letters Patent to
17 MICRON TECHNOLOGY, INC. and to KMT SEMICONDUCTOR, LTD. for
18 the joint use of MICRON TECHNOLOGY, INC. and KMT
19 SEMICONDUCTOR, LTD., its successors or assigns.

20 INVENTORS further agree to execute, at the request and expense of
21 MICRON TECHNOLOGY, INC. or KMT SEMICONDUCTOR, LTD. such
22 other formal documents as may be required to fully convey the interest

1 transferred herein and will similarly execute any application papers required
2 for the filing of any division, continuation, renewal or reissue of the patent
3 application or resulting Letters Patent; and will generally do everything
4 necessary or desirable to obtain and enforce proper protection for the
5 inventions assigned hereby.

6 INVENTORS further assign to MICRON TECHNOLOGY, INC. and
7 to KMT SEMICONDUCTOR, LTD. the whole right, title and interest in the
8 inventions disclosed in the application throughout all countries foreign to the
9 United States. MICRON TECHNOLOGY, INC. and KMT
10 SEMICONDUCTOR, LTD. are hereby authorized to apply for patents relating
11 to the inventions in their own names in countries where such procedure is
12 proper; to claim the benefit of the International Convention; to file and
13 prosecute International Applications relating to the inventions under the
14 Patent Cooperation Treaty; and to file and prosecute applications relating to
15 the inventions under the European Patent Convention. INVENTORS agree
16 to execute applications relating to the inventions in those countries and under
17 those conventions where it is necessary that the same be executed by the
18 inventor, and to execute assignments of such applications and the resulting
19 Letters Patent to MICRON TECHNOLOGY, INC. and to KMT
20 SEMICONDUCTOR, LTD. as well as all other necessary papers in relation
21 to such applications and Letters Patent.
22

1 INVENTORS further warrant and covenant that no assignment, grant,
2 mortgage, license or other agreement affecting the rights and property herein
3 conveyed has been or will be made to others by the undersigned, and that the
4 full rights to convey the same as herein expressed is possessed by the
5 undersigned.

6 To be binding on the heirs, assigns, representatives and successors of
7 the undersigned and extend to the successors, assigns and nominees of the
8 Assignees.

9 (Signature) Keiji Jono Date: 10-31-2000
10 KEIJI JONO

11 State of _____)
12) ss.
County of _____)

13 BEFORE ME, this _____ day of _____ 2000, personally appeared
14 the above-named inventor, to me known to be the person who is described in
15 and who executed the foregoing assignment instrument and acknowledged to
me that he/she executed the same of his/her own free will for the purpose
therein expressed.

16 SEAL

17
18 _____
19 Notary or Consular Officer
My Commission Expires: _____
20
21
22

1 (Signature) Hirokazu Ueda Date: 10-31-00
HIROKAZU UEDA

2 State of _____)
3) ss.
4 County of _____)

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10 SEAL

11 _____
12 Notary or Consular Officer
13 My Commission Expires: _____

14 (Signature) ~~Hiro~~ Hiroyuki Watanabe Date: 10-31-2000
15 HIROYUKI WATANABE

16 State of _____)
17) ss.
18 County of _____)

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Donald Brent Kenady Reg. No. 40,045
James E. Lake Reg. No. 44,854
Bernard Berman Reg. No. 37,279

and also attorneys Michael L. Lynch (Reg. No. 30,871) and Lia Pappas Dennison (Reg. No. 34,095) of Micron Technology, Inc., as its attorneys with full power of substitution to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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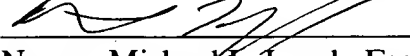
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Attn: Frederick M. Fliegel, Ph.D.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Telephone: (509) 624-4276
Facsimile: (509) 838-3424

MICRON TECHNOLOGY, INC.

Dated: 11/9/00

By: 
Name: Michael L. Lynch, Esq.
Title: Chief Patent Counsel

Attachments: Copy of Assignment; Copy of Board of Directors' Resolution

MICRON TECHNOLOGY, INC.
CERTIFIED COPY OF RESOLUTIONS



RECEIVED
DEC 05 2000
TECHNOLOGY CENTER 2800

I, Jan R. Reimer, the Assistant Secretary of Micron Technology, Inc. do hereby certify, that the resolutions attached hereto represent a complete, true and correct copy of the resolutions duly adopted by the Board of Directors of Micron Technology, Inc., a corporation duly organized and existing under the laws of the State of Delaware, at a meeting duly held on March 25, 1996, a quorum being present, and have been entered into the minutes of said meeting; that I am the keeper of the corporate seal and of the minutes and records of this Corporation; and that the said resolutions have not been rescinded or modified.

The resolutions attached hereto are in conformity with the Articles of Incorporation and Bylaws of the Corporation and are now in full force and effect.

I further certify that the person whose name and signature is set out below is the person authorized to act for said corporation in transactions with and pursuant to the foregoing resolutions, and that such person is now duly qualified and acting in his respective capacity:

NAME AND TITLE

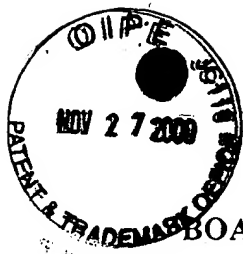
SIGNATURE

Michael L. Lynch, Assistant General
Counsel for Intellectual Property

IN WITNESS WHEREOF, I have hereunto subscribed my name and affixed the seal of the said corporation, this 16th day of May, 1996.

Jan R. Reimer, Assistant Secretary

(SEAL)



MICRON TECHNOLOGY, INC.
BOARD OF DIRECTORS RESOLUTIONS

RECEIVED
DEC 05 2000
TECHNOLOGY CENTER 2800

WHEREAS, certain key employees require the authority to execute certain documents on behalf of the Company in order to enable them to effectively and efficiently carry out their responsibilities and duties to the Company.

NOW THEREFORE BE IT RESOLVED, that the Board hereby approves and authorizes Mr. Michael L. Lynch, Assistant General Counsel for Intellectual Property, to execute on behalf of the Company, documents pertaining to the Company's patent prosecution matters, including but not limited to documents relating to representation before a patent examining authority, patent terms and other patent prosecution procedures, both in the United States and other countries, upon such terms and conditions as the General Counsel of the Company shall deem necessary or appropriate.

1 (Signature) Hirokazu Ueda Date: 10-31-00
HIROKAZU UEDA

2 State of _____)
3) ss.
4 County of _____)

5 BEFORE ME, this _____ day of _____ 2000, personally appeared
6 the above-named inventor, to me known to be the person who is described in
7 and who executed the foregoing assignment instrument and acknowledged to
8 me that he/she executed the same of his/her own free will for the purpose
9 therein expressed.

10 SEAL

11 _____
12 Notary or Consular Officer
13 My Commission Expires: _____

14 (Signature) ~~Hiro~~ Hiroyuki Watanabe Date: 10-31-2000
15 HIROYUKI WATANABE

16 State of _____)
17) ss.
18 County of _____)

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